

Day : Wednesday

Date: 3/2/2005

Time: 14:43:34

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = SIM

First Name = KIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10670975</u>	Not Issued	019	09/25/2003	STITCHED MICRO-VIA TO ENHANCE ADHESION AND MECHANICAL STRENGTH	SIM, KIAN SIN
<u>10673605</u>	Not Issued	061	09/29/2003	DIFFUSION BARRIER LAYER FOR LEAD FREE PACKAGE SUBSTRATE	SIM, KIAN SIN
<u>10691135</u> ✓	Not Issued	030	10/21/2003	ROBUST INTERLOCKING VIA	SIM, KIAN SIN
<u>10917140</u> ✓	Not Issued	041	08/11/2004	ROBUST INTERLOCKING VIA	SIM, KIAN SIN

Inventor Search Completed: No Records to Display.

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<input type="text" value="sim"/>	<input type="text" value="kian"/>	<input type="button" value="Search"/>

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Day : Wednesday

Date: 3/2/2005
Time: 14:44:14**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = SIR

First Name = JIUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10691135	Not Issued	030	10/21/2003	ROBUST INTERLOCKING VIA	SIR, JIUN HANN
10882468	Not Issued	030	06/30/2004	INTERCONNECTS WITH DIRECT METALIZATION AND CONDUCTIVE POLYMER	SIR, JIUN HANN
10902446	Not Issued	030	07/28/2004	INTERCONNECTS WITH INTERLOCKS	SIR, JIUN HANN
10917140	Not Issued	041	08/11/2004	ROBUST INTERLOCKING VIA	SIR, JIUN HANN

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